

POZNAN UNIVERSITY OF TECHNOLOGY

EUROPEAN CREDIT TRANSFER AND ACCUMULATION SYSTEM (ECTS)

COURSE DESCRIPTION CARD - SYLLABUS

Course name

Digital systems design [N1EiT1>TC]

Course

Field of study Year/Semester

Electronics and Telecommunications 2/3

Area of study (specialization) Profile of study

general academic

Level of study Course offered in

first-cycle Polish

Form of study Requirements part-time compulsory

Number of hours

Lecture Laboratory classes Other

30 0

Tutorials Projects/seminars

15 0

Number of credit points

4,00

Coordinators Lecturers

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Prerequisites

none

Course objective

none

Course-related learning outcomes

none

Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

none

Programme content

none

Course topics

none

Teaching methods

none

Bibliography

none

Breakdown of average student's workload

	Hours	ECTS
Total workload	180	8,00
Classes requiring direct contact with the teacher	75	3,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	105	5,00